Figure 1: The baseband transmitter structure of a WPAN System
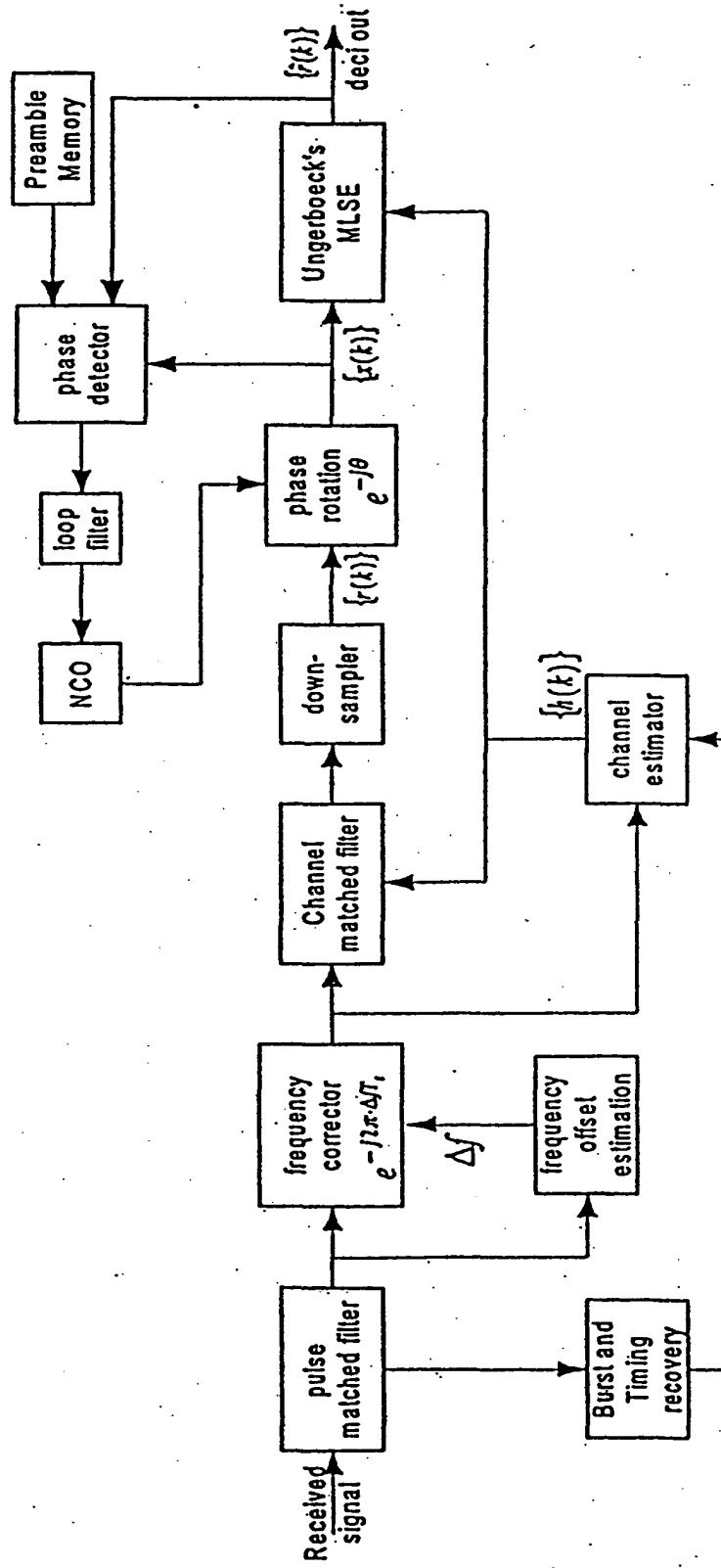
Figure 2: The data structure of a WPAN burst

Figure 3: The baseband receiver structure of WPAN: small loop
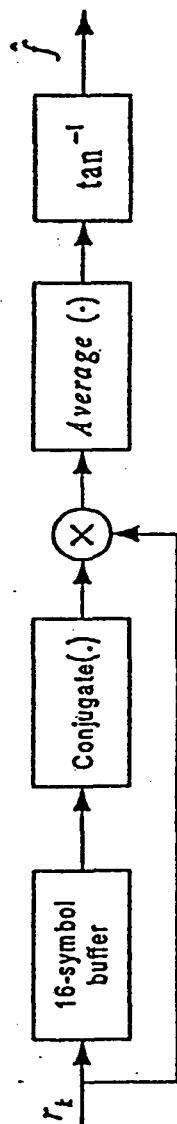
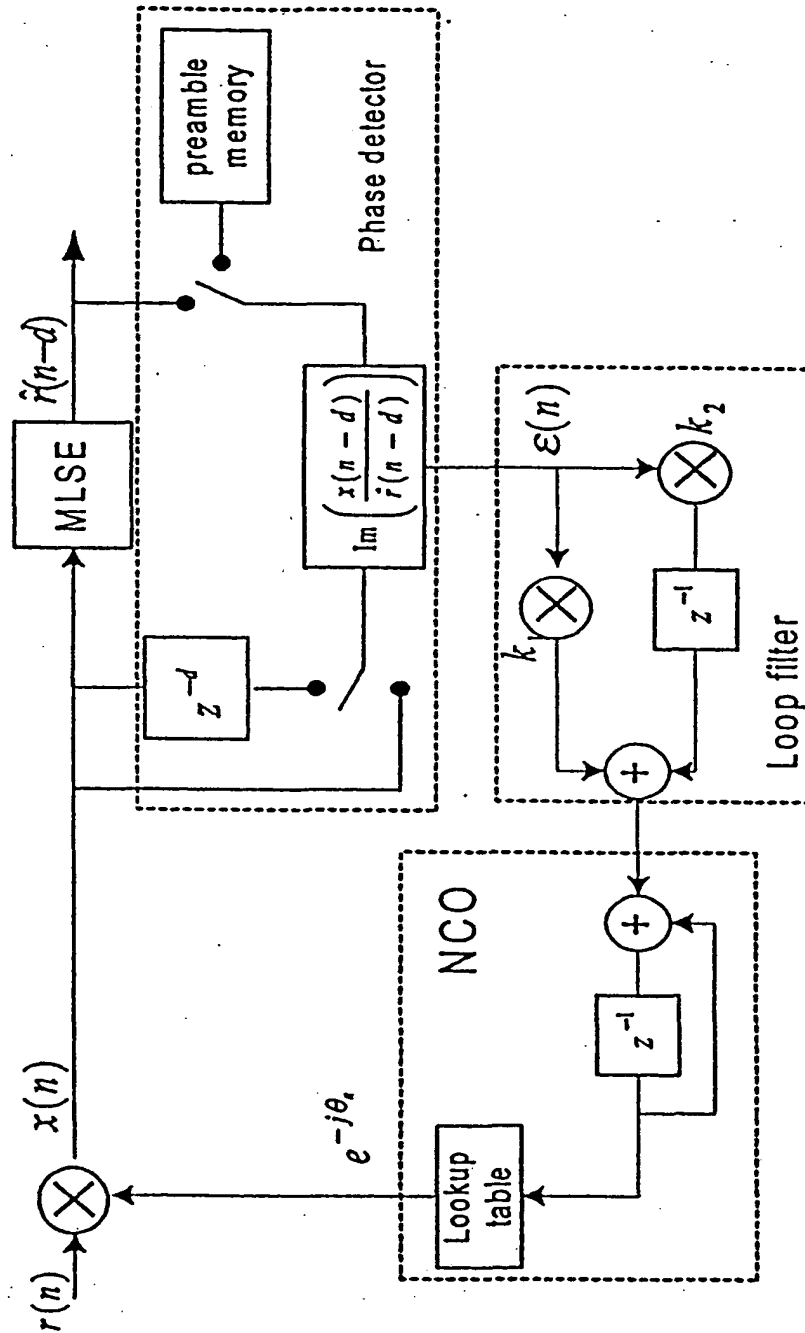Figure 4: The circuit of the frequency offset estimator

Figure 5: The carrier recovery block

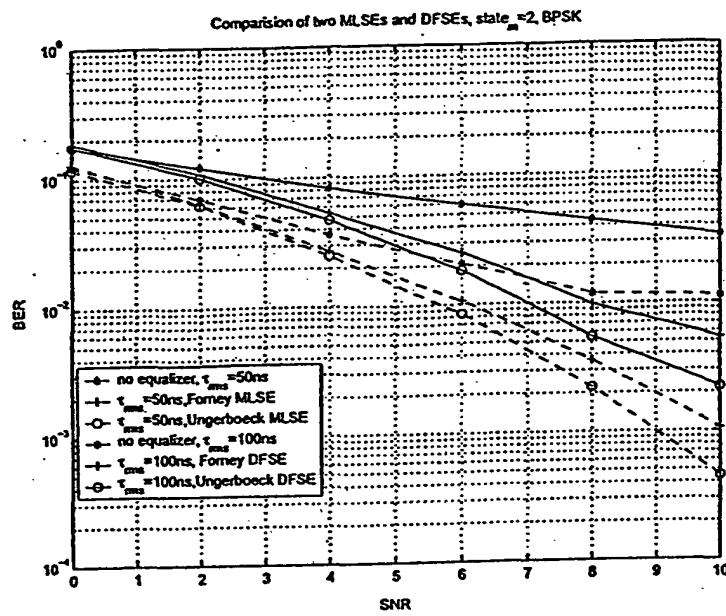Comparision of two MLSEs and DFSEs, state$_m$=2, BPSK

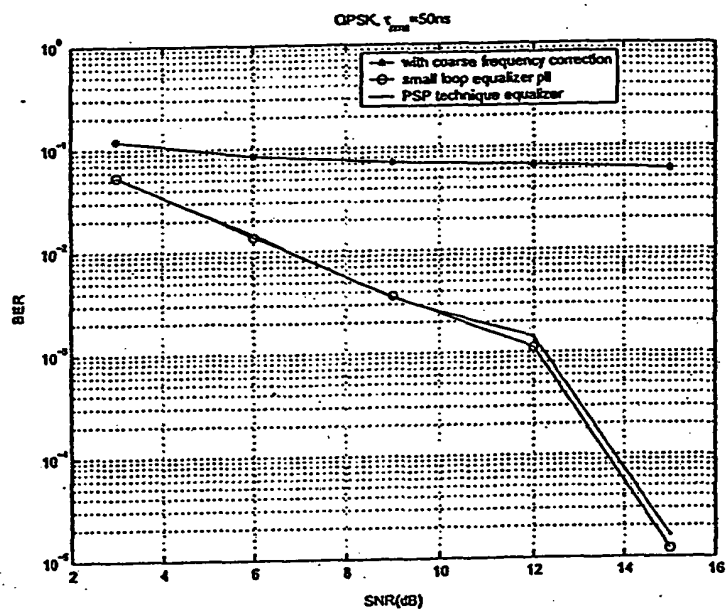Figure 6: Comparison of two MLSEs and theirs DFSE, joint channel and data estimation, BPSK

Figure 7: The BER comparison of two receiver architectures